F I G. 1

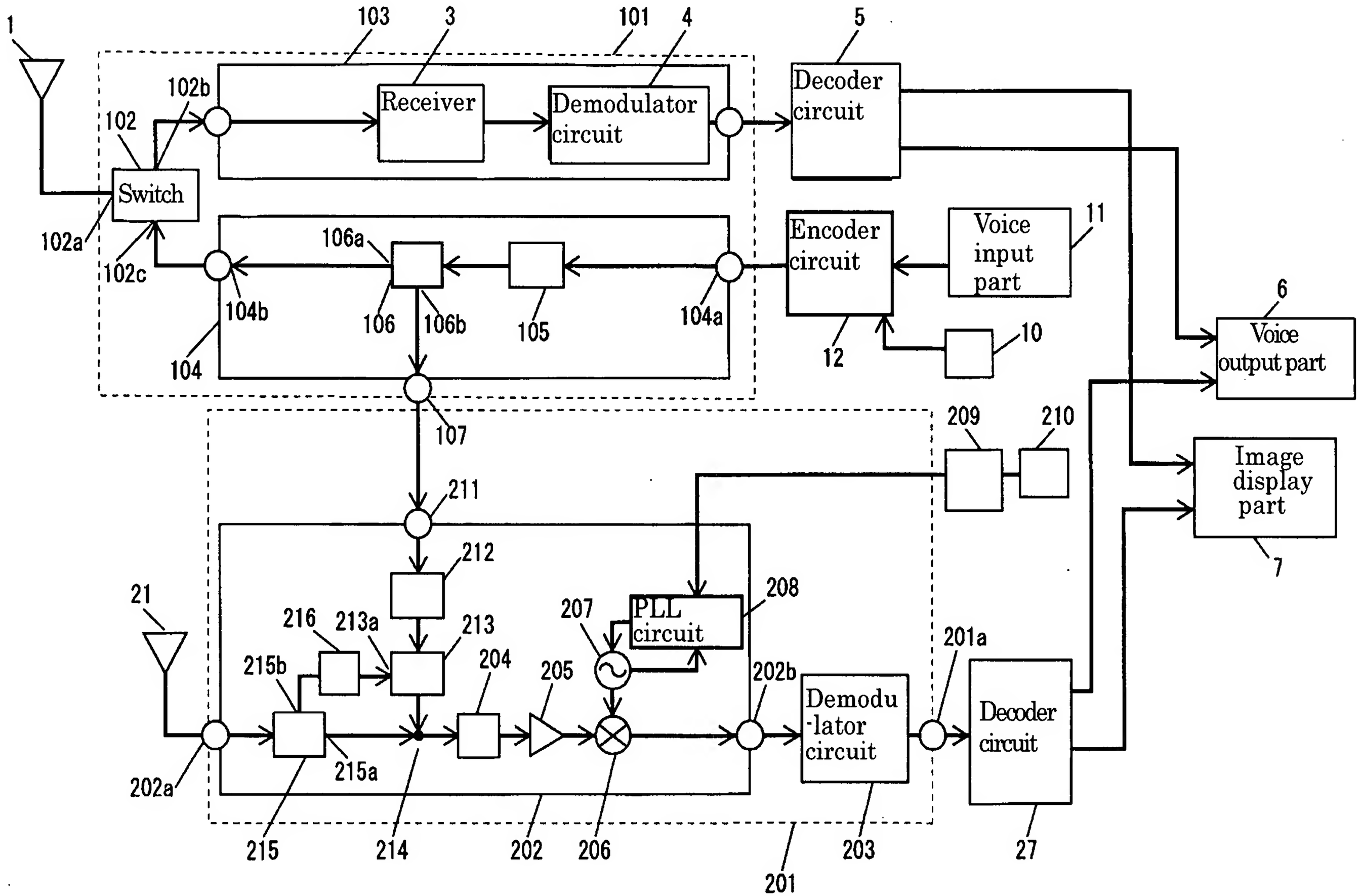

1
103
3
101
4
5
102b
102
Receiver
Demodulator circuit
Decoder circuit
Switch
102a
102c
106a
Encoder circuit
Voice input part
11
6
104b
106
106b
105
104a
12
10
Voice output part
104
107
209
210
211
Image display part
212
7
21
208
PLL circuit
207
216
213a
213
215b
204
205
202b
201a
Demodu -lator circuit
Decoder circuit
202a
215a
215
214
202
206
203
27
201

FIG. 2

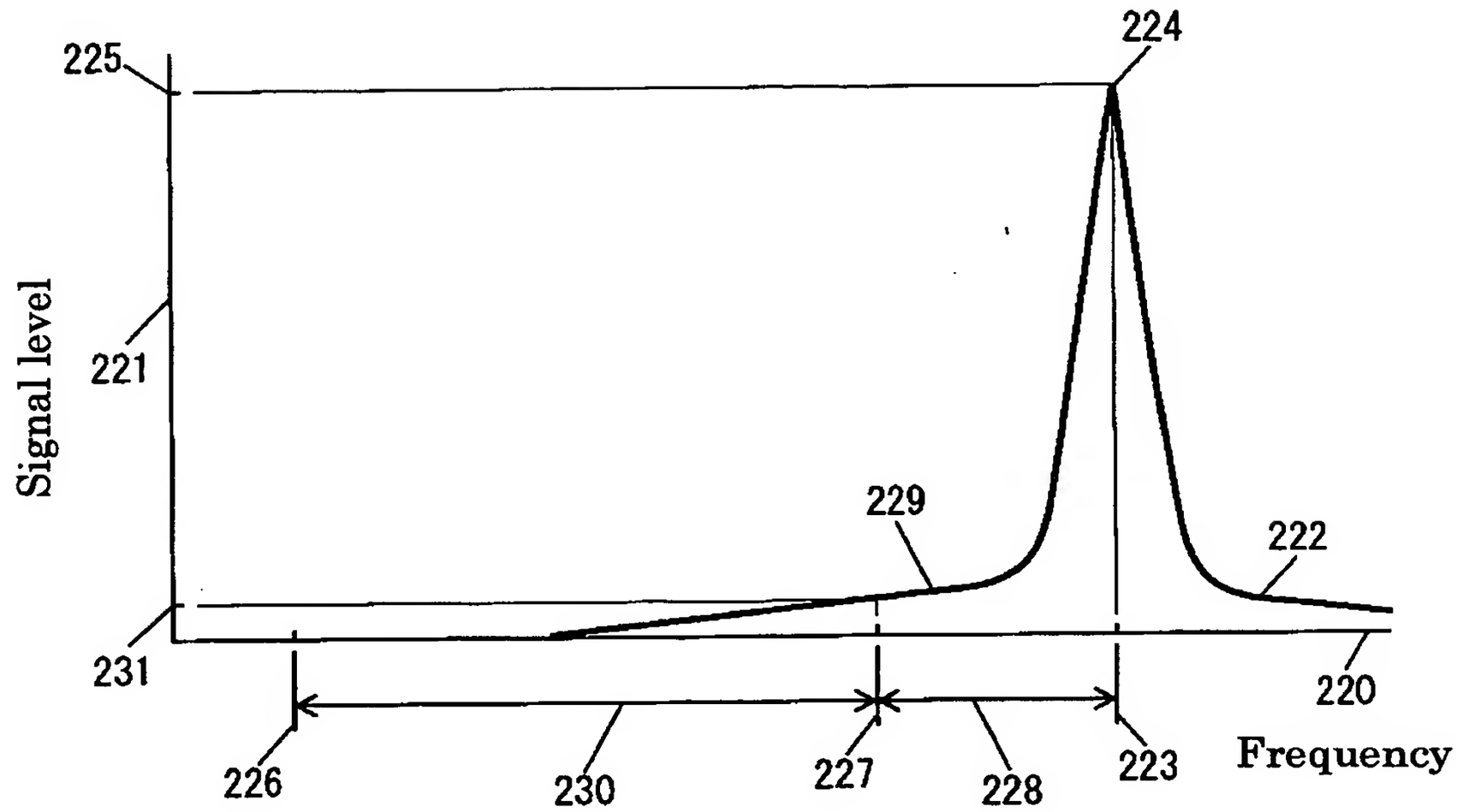

224
225
221
Signal level
229
222
231
220
226
230
227
228
223
Frequency

FIG. 3

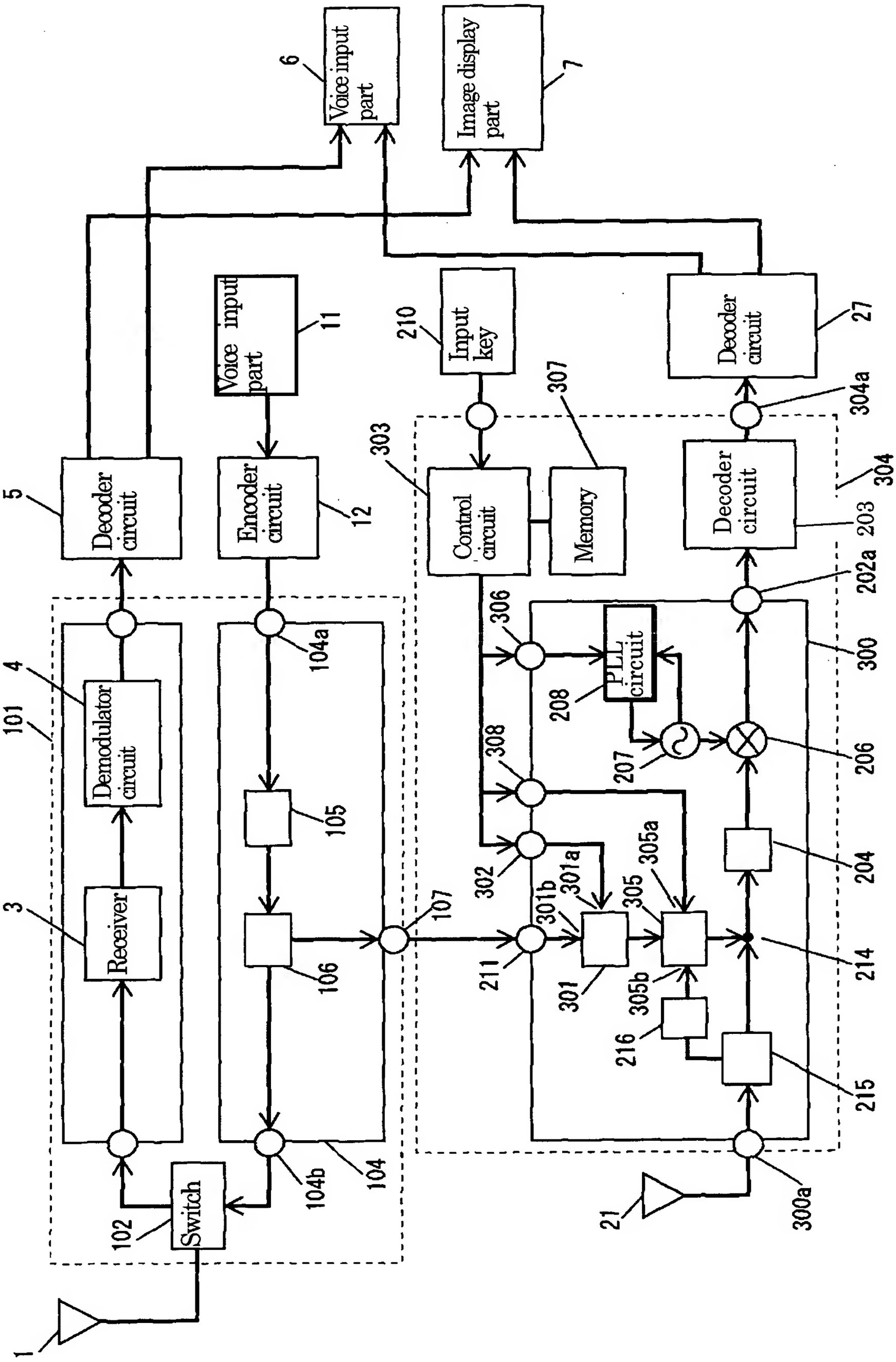

Voice input part
6
Image display part
7
Decoder circuit
5
Demodulator circuit
4
101
Receiver
3
Switch
102
1
Encoder circuit
12
Voice input part
11
104a
105
106
107
104b
104
Input key
210
Control circuit
303
Memory
307
Decoder circuit
27
304a
Decoder circuit
203
304
202a
306
PLL circuit
208
207
308
300
206
302
301b
301a
305
305a
204
211
301
305b
216
214
215
21
300a

FIG. 4

352 Receiving channel	353 Phase shifter control voltage	351 PLL data	354 Level regulator	355
1 CH	▪	▪	▪	
2 CH	▪	▪	▪	
▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪	
CH	▪	▪	▪	
▪ ▪ ▪ ▪	▪ ▪ ▪ ▪	▪ ▪ ▪ ▪	▪ ▪ ▪ ▪	
CH	▪	▪	▪	

FIG. 5

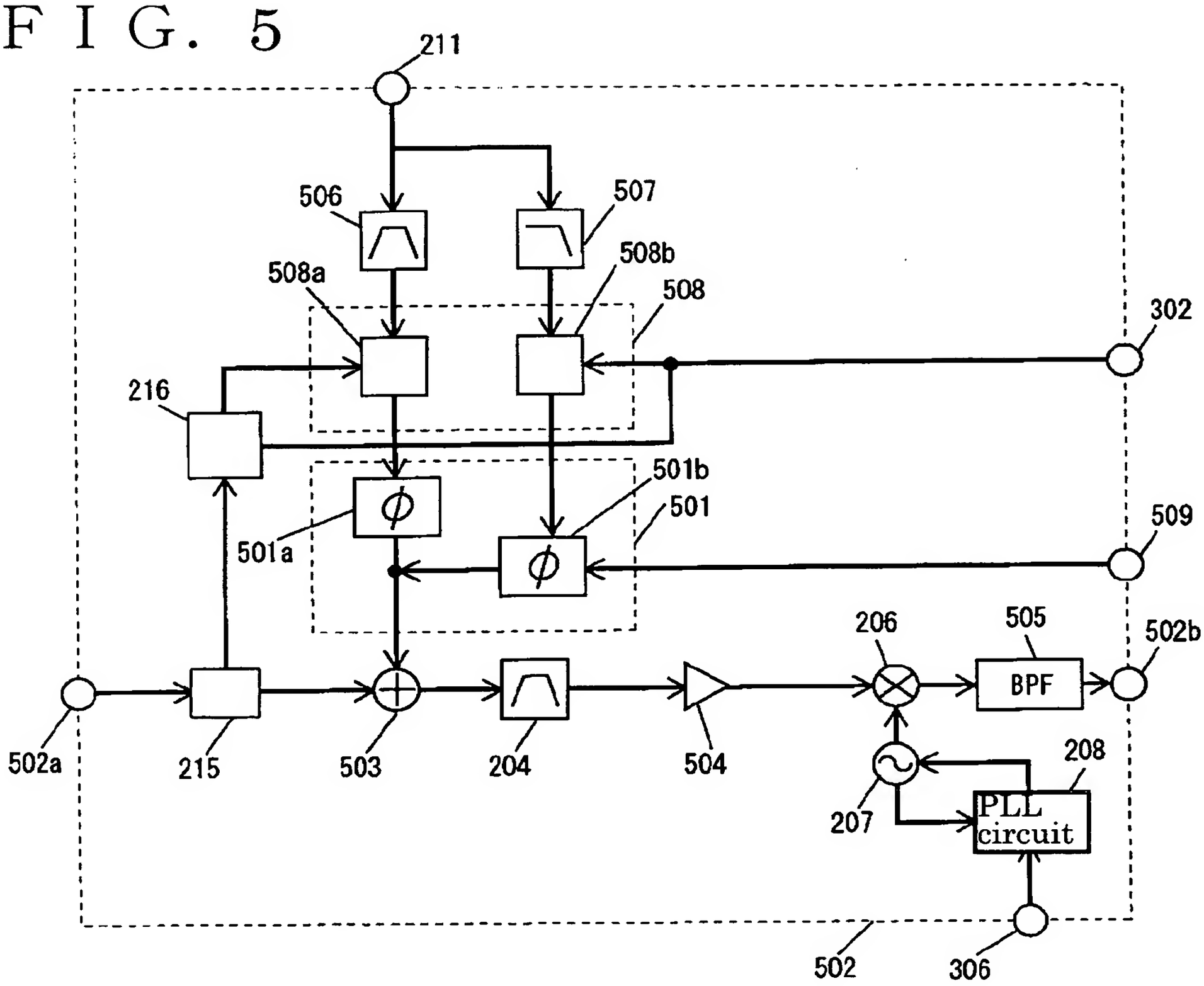

211
506
507
508b
508a
508
302
216
501b
501
501a
509
206
505
502b
BPF
502a
215
503
204
504
208
207
PLL
circuit
502
306

FIG. 6

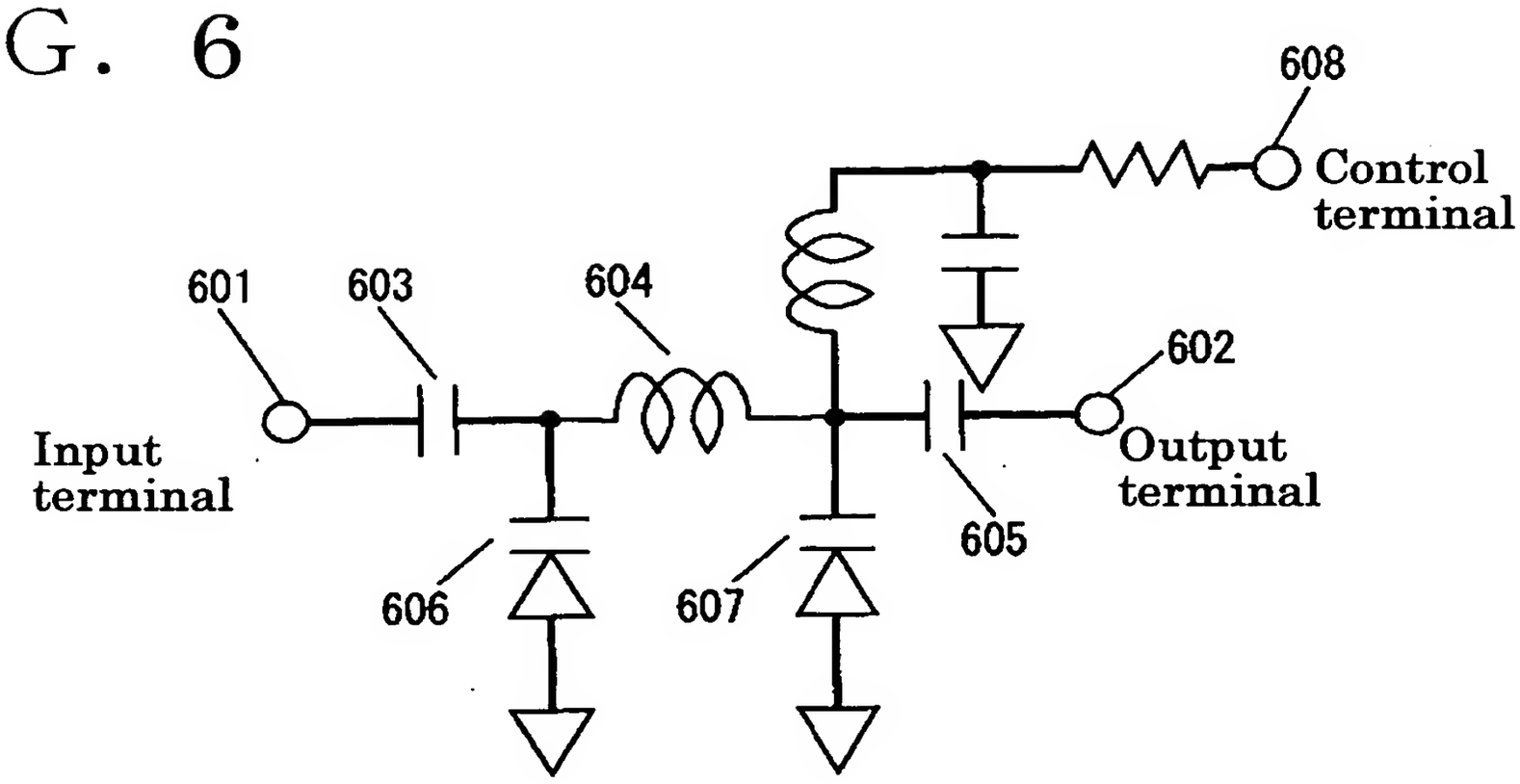

608
Control terminal
601
603
604
602
Input terminal
Output terminal
605
606
607

FIG. 7

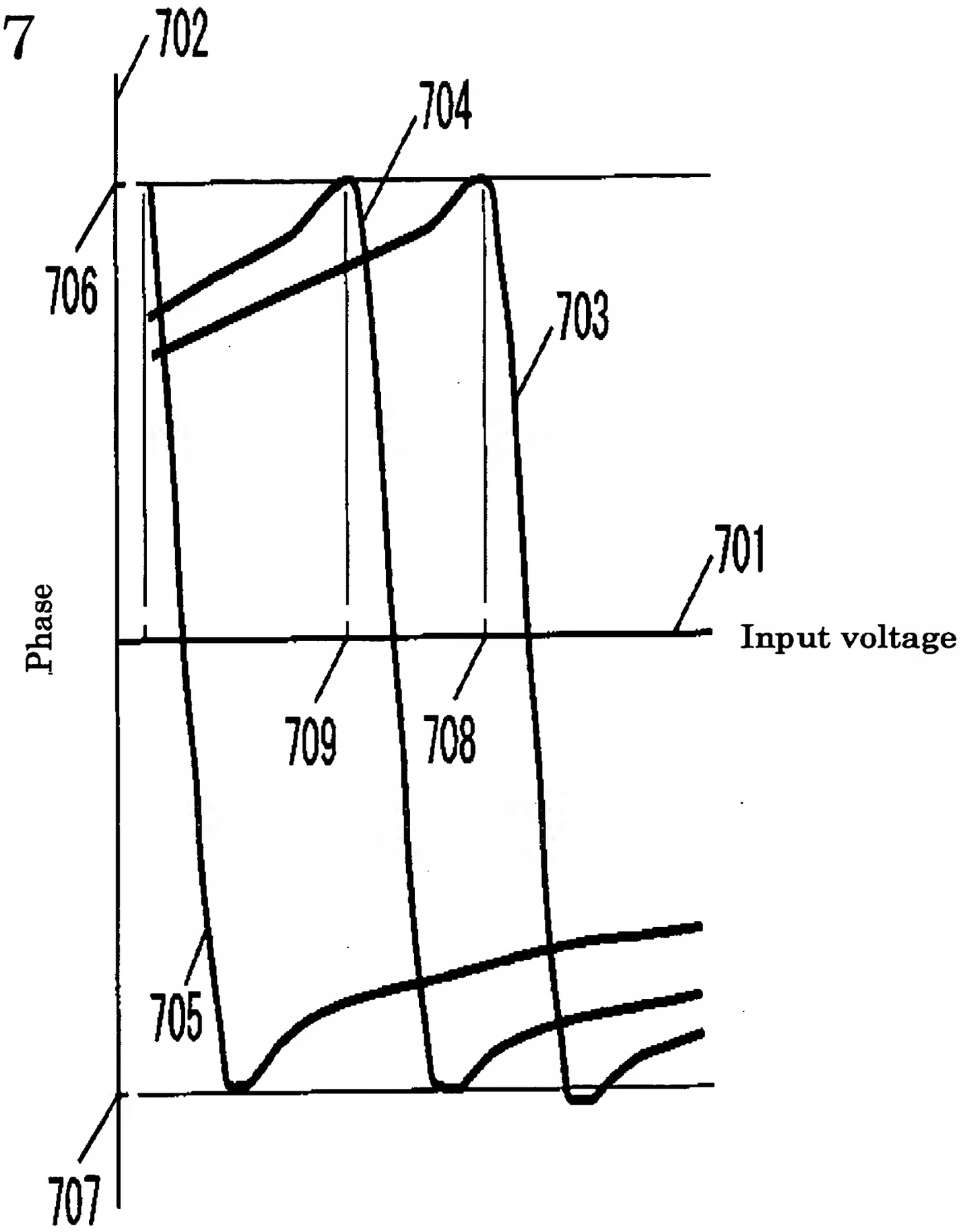


FIG. 8

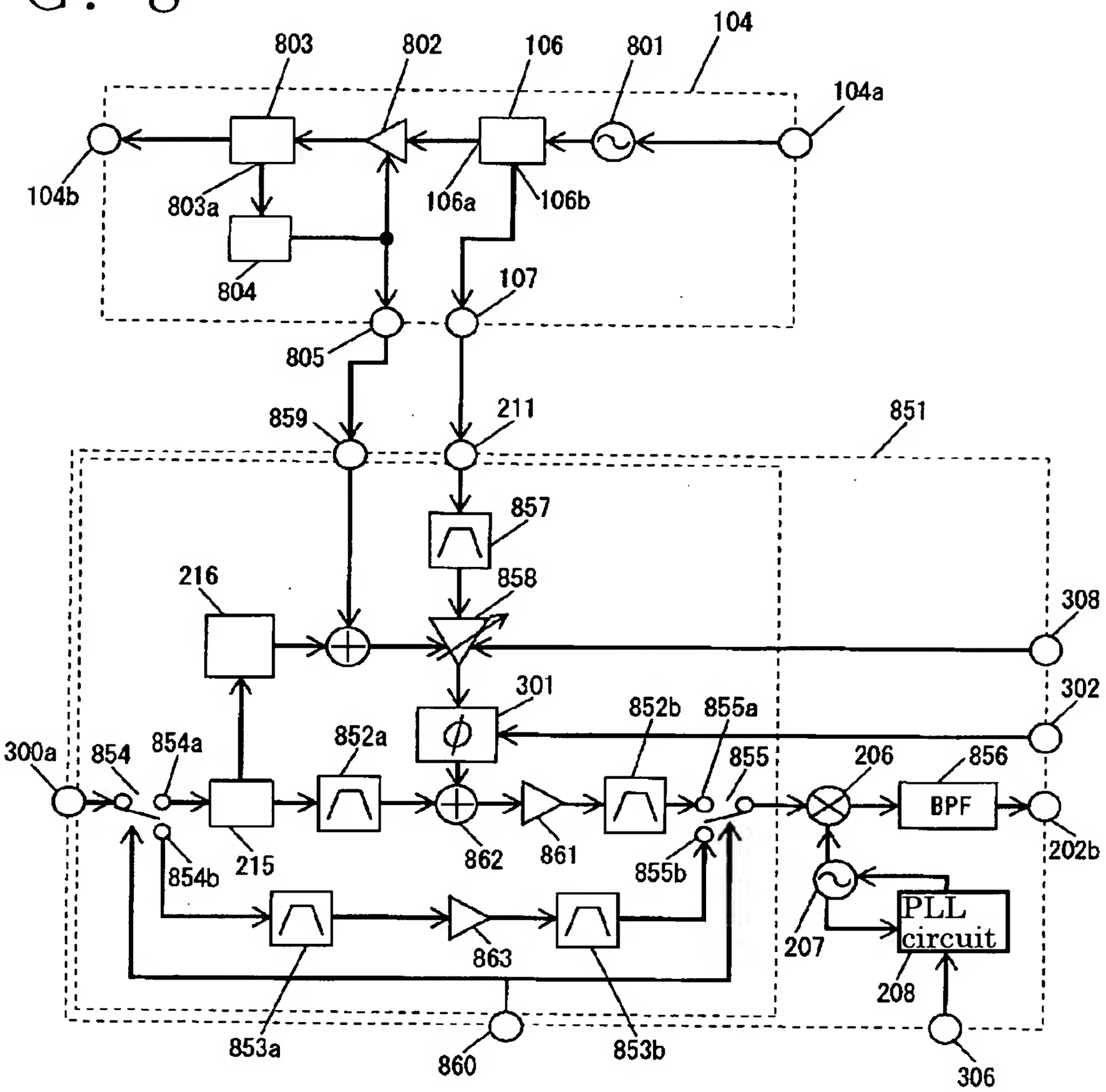

803
802
106
801
104
104a
104b
803a
106a
106b
804
107
805
859
211
851
857
858
216
308
301
302
852b
855a
852a
300a
854
854a
855
206
856
BPF
202b
215
862
861
854b
855b
PLL
circuit
207
208
863
853a
860
853b
306

FIG. 9

Receiving channel	Phase shifter Control voltage	Switch	PLL data	AGC	Amplifier	Amplifier
1 CH	▪	▪	▪	▪	▪	▪
2 CH	▪	▪	▪	▪	▪	▪
▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪ ▪
CH	▪	▪	▪	▪	▪	▪
▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪	▪ ▪ ▪ ▪ ▪
CH	▪	▪	▪	▪	▪	▪

FIG. 10

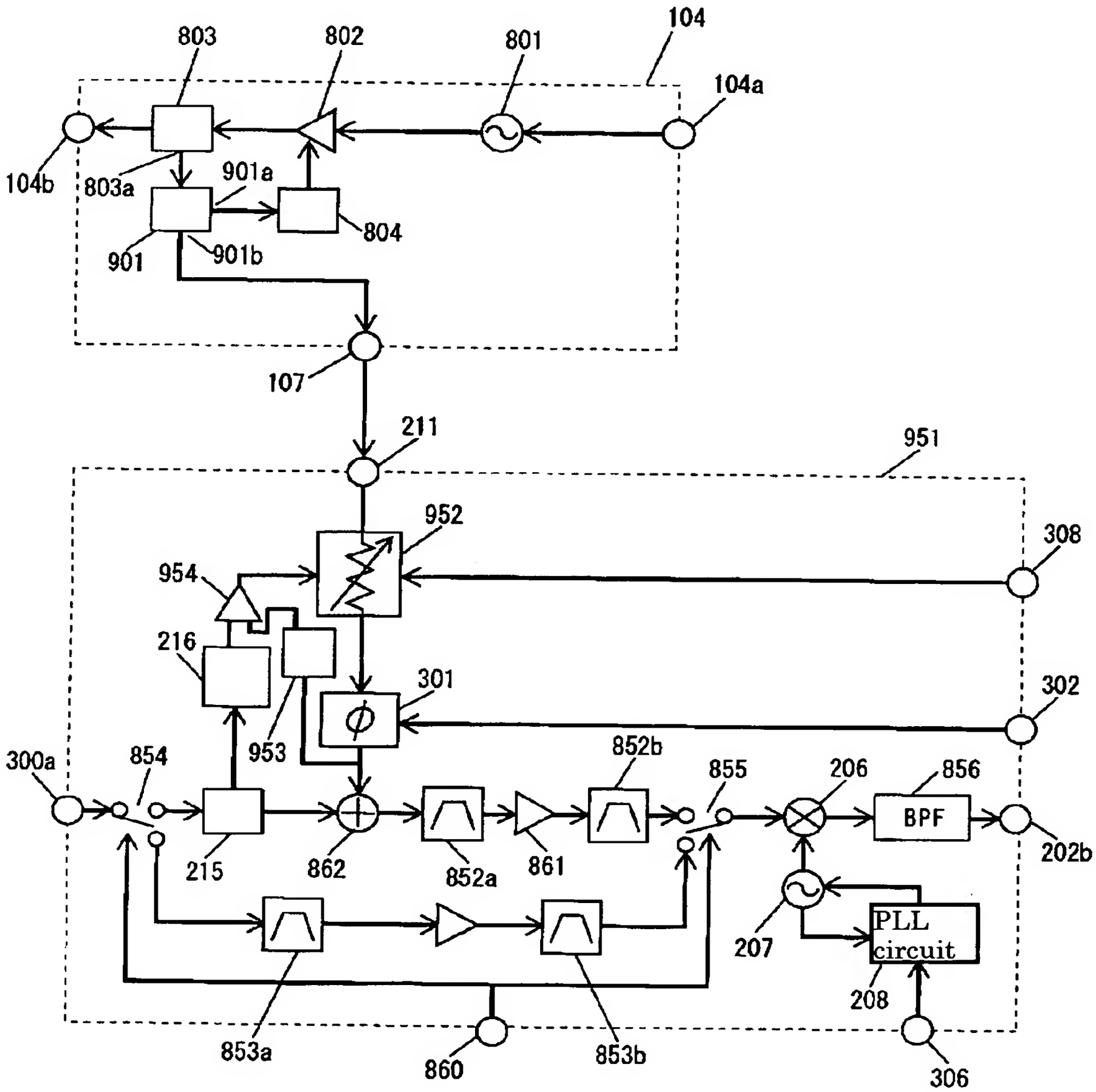

803
802
801
104
104a
104b
803a
901a
804
901
901b
107
211
951
952
308
954
216
301
302
953
300a
854
852b
855
206
856
BPF
202b
215
862
852a
861
207
PLL
circuit
208
853a
860
853b
306

FIG. 11

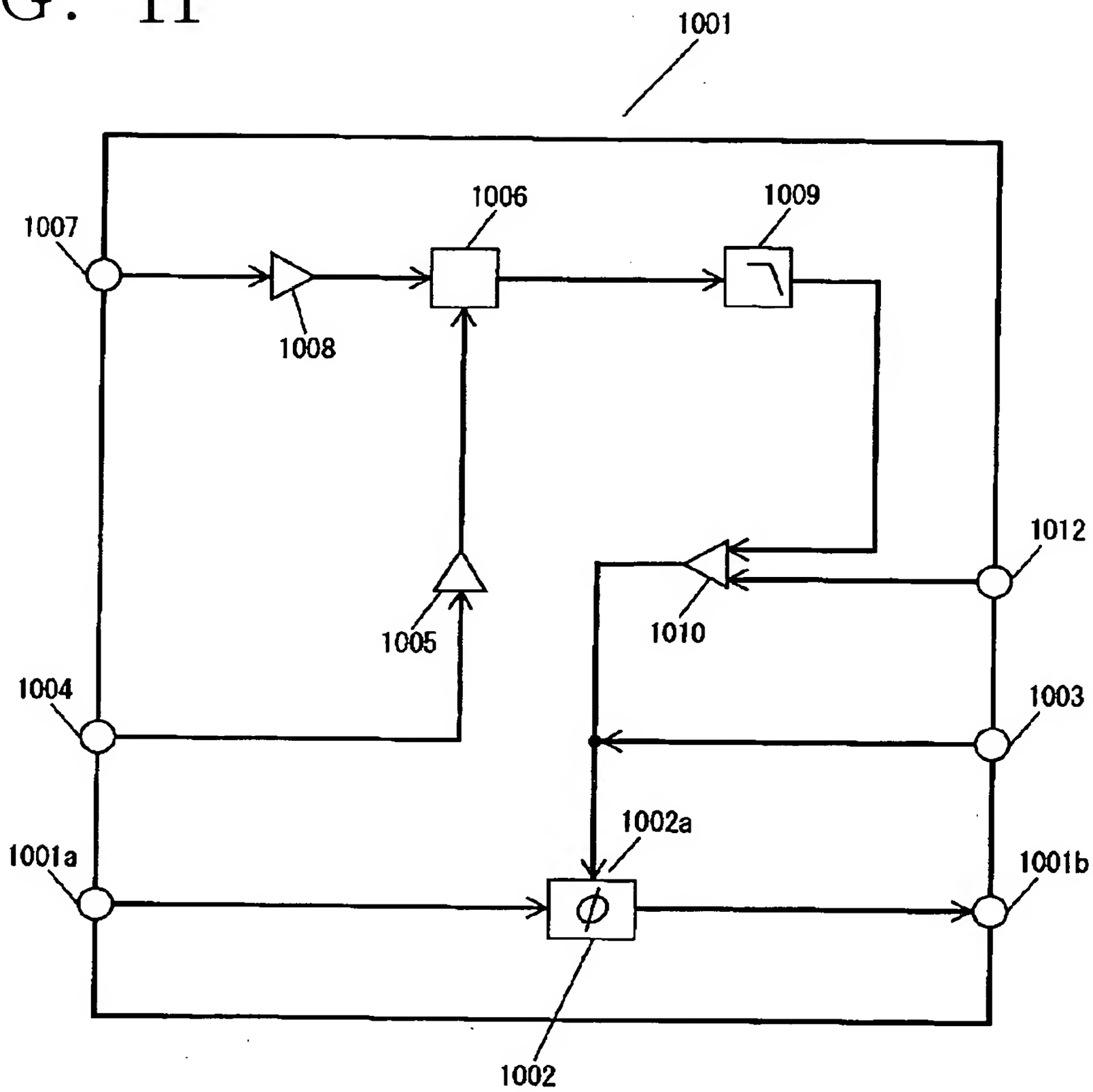
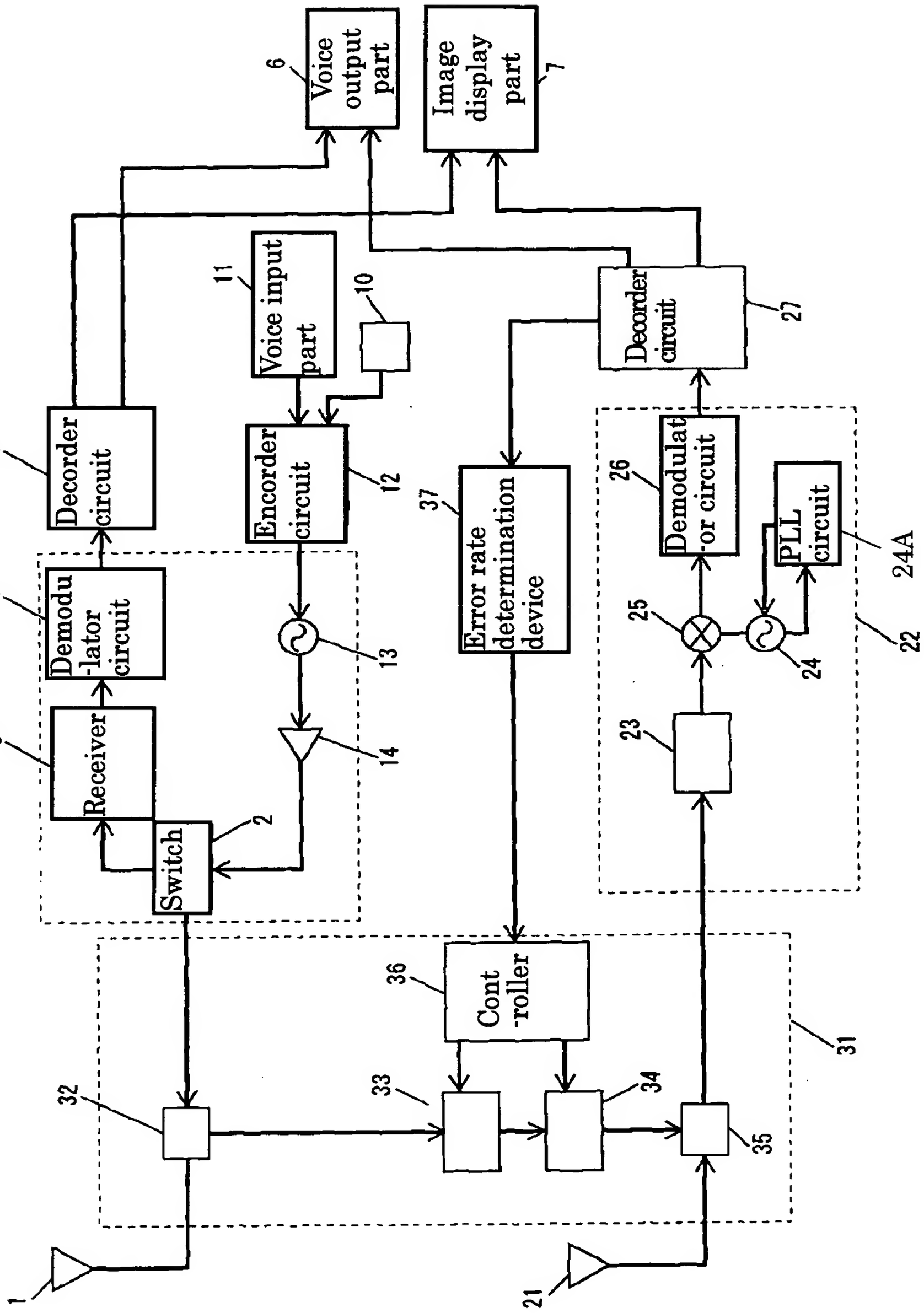

1001
1007
1006
1009
1008
1012
1010
1005
1004
1003
1002a
1001a
1001b
1002

FIG. 12


Voice output part
6
Image display part
7
Voice input part
11
10
Decorder circuit
5
Encorder circuit
12
Demodu -lator circuit
4
Receiver
3
Switch
2
13
14
Error rate determination device
37
Decorder circuit
27
Demodulat -or circuit
26
PLL circuit
24A
25
24
23
22
Cont -roller
36
31
32
33
34
35
1
21